



## University Program Software Selection

Americas University Program 2013

### Custom Integrated Circuits Bundle

#### The Custom IC Bundle Software Reference List:

| <u>Product</u>  | <u>Product No.</u> | <u>Release Stream</u> |
|---|--------------------|-----------------------|
| <b>Design Environment</b>   |                    |                       |
| Virtuoso <sup>®</sup> Simulation Environment                              | 206                | IC615                 |
| Virtuoso <sup>®</sup> AMS Designer Environment                            | 70000              | IC615                 |
| Virtuoso <sup>®</sup> Analog Design Environment - XL                      | 95210              | IC615                 |
| Virtuoso <sup>®</sup> Analog Design Environment - GXL                     | 95220              | IC615                 |
| <b>Design Entry</b>   |                    |                       |
| Cadence <sup>®</sup> SKILL Development Environment                        | 900                | IC615                 |
| Virtuoso <sup>®</sup> Schematic VHDL Interface                            | 21060              | IC615                 |
| Virtuoso <sup>®</sup> Schematic Editor Verilog <sup>®</sup> Interface     | 21400              | IC615                 |
| Virtuoso <sup>®</sup> Schematic Editor - XL                               | 95115              | IC615                 |
| Virtuoso <sup>®</sup> Analog Oasis Run-Time Option                        | 32100              | IC615                 |
| <b>Circuit Simulation</b>   |                    |                       |
| Virtuoso <sup>®</sup> Schematic Editor HSPICE Interface <sup>1</sup>      | 276                | IC615                 |
| Virtuoso <sup>®</sup> Analog HSPICE Interface Option <sup>1</sup>         | 32760              | IC615                 |
| Virtuoso <sup>®</sup> Spectre <sup>®</sup> Circuit Simulator              | 38500              | MMSIM121              |
| Virtuoso <sup>®</sup> UltraSim Simulator                                  | 33400              | MMSIM121              |
| Virtuoso <sup>®</sup> Multi-mode Simulation Power Option                  | 91400              | MMSIM121              |
| Virtuoso <sup>®</sup> Multi-mode Simulation CPU Accelerator option        | 91500              | MMSIM121              |
| Virtuoso <sup>®</sup> Spectre <sup>®</sup> RF Simulation Option for 38500 | 38520              | MMSIM121              |
| Virtuoso <sup>®</sup> RelXpert  | 33580              | MMSIM121              |
| Virtuoso <sup>®</sup> Multi-mode Simulation with AP Simulator             | 90003              | MMSIM121              |
| <b>Interfaces</b>   |                    |                       |
| Virtuoso <sup>®</sup> EDIF 200 Reader                                     | 940                | IC615                 |
| Virtuoso <sup>®</sup> EDIF 200 Writer                                     | 945                | IC615                 |
| Cadence <sup>®</sup> Design Framework Integrator's Toolkit                | 12141              | IC615                 |

<sup>1</sup> requires additional licenses that must be obtained from Synopsys



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### The Custom IC Bundle Software Reference List, continued

| <b><u>Product</u></b>   | <b><u>Product No.</u></b> | <b><u>Release Stream</u></b> |
|---|---------------------------|------------------------------|
| <b>Physical Verification</b>  |                           |                              |
| Dracula <sup>®</sup> Graphical User Interface                                     | 365                       | IC615                        |
| Dracula <sup>®</sup> Physical Verification and Extractor Suite                    | 70520                     | IC615                        |
| Diva <sup>®</sup> Physical Verification and Extractor Suite                       | 71520                     | IC615                        |
| Assura <sup>™</sup> Design Rule Checker   | 72110                     | ASSURA41                     |
| Assura <sup>™</sup> Layout vs. Schematic Verifier                                 | 72120                     | ASSURA41                     |
| Assura <sup>™</sup> Graphical User Interface Option                               | 72140                     | ASSURA41                     |
| Assura <sup>™</sup> Multiprocessor Option   | 72150                     | ASSURA41                     |
| Virtuoso <sup>®</sup> QRC Extraction - L  | QRCX100                   | PVE121                       |
| Virtuoso <sup>®</sup> QRC Extraction - XL   | QRCX300                   | PVE121                       |
| Virtuoso <sup>®</sup> Advanced Analysis GXL option                                | QRCX310                   | PVE121                       |
| Cadence <sup>®</sup> QRC Display Technology GXL Option                            | QRCX330                   | PVE121                       |
| Cadence <sup>®</sup> QuickView Layout and Mask Data Viewer                        | K2200                     | PVE121                       |
| Cadence <sup>®</sup> Physical Verification System Design Rule Checker XL          | 96210                     | PVE121                       |
| Cadence <sup>®</sup> Physical Verification System Layout vs. Schematic Checker XL | 96220                     | PVE121                       |
| Cadence <sup>®</sup> Physical Verification System Results Manager                 | 96240                     | PVE121                       |
| Cadence <sup>®</sup> Physical Verification System graphic LVS Debugger            | 96250                     | PVE121                       |
| Cadence <sup>®</sup> Physical Verification System Constraint Validator            | 96300                     | PVE121                       |
| Pcell Generator   | PASPCG                    | PAS31                        |
| Graphical Technology Editor   | PASGTE                    | PAS31                        |
| Generator for Assura <sup>™</sup> compatible verification decks                   | PASASG                    | PAS31                        |
| Generator for Diva <sup>®</sup> compatible verification decks                     | PASDIG                    | PAS31                        |
| Generator for Dracula <sup>®</sup> compatible verification decks                  | PASDRG                    | PAS31                        |
| Error Cell Generator  | PASECG                    | PAS31                        |



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### Digital Integrated Circuits Bundle

#### The Digital IC Bundle Software Reference List

| <b><u>Product</u></b>                                     | <b><u>Product No.</u></b> | <b><u>Release Stream</u></b> |
|---|---------------------------|------------------------------|
| <b>Place &amp; Route and Timing</b>                       |                           |                              |
| Virtuoso <sup>®</sup> Layout Suite - GXL <sup>1</sup>     | 95321                     | IC615                        |
| Virtuoso <sup>®</sup> DFM Option                          | 95311                     | IC615                        |
| Cadence <sup>®</sup> Chip Assembly Router <sup>2</sup>    | 3300                      | IC615                        |
| <b>Design for Manufacturing</b>                           |                           |                              |
| Virtuoso <sup>®</sup> Power System – L                    | VPS100                    | IC615                        |
| Virtuoso <sup>®</sup> Power System – XL                   | VPS200                    | IC615                        |
| Encounter Power System - L                                | EPS100                    | ETS120                       |
| Encounter Power System - XL                               | EPS200                    | ETS120                       |
| EPS Advanced Analysis GXL Option                          | EPS201                    | ETS120                       |
| <b>Signal Integrity</b>                                   |                           |                              |
| Encounter <sup>™</sup> Timing System - XL                 | FE725                     | ETS120                       |
| Encounter <sup>™</sup> Timing System - L                  | FE625                     | ETS120                       |
| ETS Advanced Analysis GXL Option                          | FE830                     | ETS120                       |
| Encounter <sup>™</sup> Library Characterizer- XL          | ELC200                    | ETS120                       |
| Pacific Static Noise Analyzer for Custom Digital ICs      | CM00100                   | PACIFIC61                    |
| <b>Silicon Virtual Prototyping</b>                        |                           |                              |
| Encounter <sup>™</sup> Digital Implementation System - XL | EDS200                    | EDI120                       |
| Encounter <sup>™</sup> Low Power GXL Option               | EDS10                     | EDI120                       |
| Encounter <sup>™</sup> Advanced Node GXL option           | EDS30                     | EDI120                       |
| Encounter <sup>™</sup> Mixed Signal GXL Option            | EDS20                     | EDI120                       |
| Encounter <sup>™</sup> DFM GXL Option                     | EDS50                     | MVS121                       |
| Litho Physical Analyzer                                   | LPA108                    | MVS121                       |
| Distributed Process for 8 CPUs                            | LPA109                    | MVS121                       |

<sup>1</sup> includes CIF reader/writer and Virtuoso Stream Interface

<sup>2</sup> includes GDSII/EDIF and LEF/DEF interfaces



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### The Digital IC Bundle Software Reference List, continued

#### Test

|  |       |       |
|--|-------|-------|
| Architect Advanced Option to RC                | ET021 | ET121 |
| Encounter <sup>™</sup> True Time Test Advanced | ET023 | ET121 |
| Encounter <sup>™</sup> Diagnostics Basic       | ET010 | ET121 |

#### Formal Verification

|   |        |            |
|---|--------|------------|
| Encounter <sup>™</sup> Conformal - GXL          | CFM300 | CONFRML121 |
| Encounter <sup>™</sup> Conformal Low Power - XL | CFM500 | CONFRML121 |

#### Synthesis

|  |        |         |
|--|--------|---------|
| Encounter <sup>™</sup> RTL Compiler - L                      | RC110  | RC121   |
| Encounter <sup>™</sup> RTL Compiler - XL                     | RC200  | RC121   |
| Encounter <sup>™</sup> RTL Compiler Low Power Option         | RC310  | RC121   |
| Encounter <sup>™</sup> RTL Compiler Advanced Physical Option | RC340  | RC121   |
| C-to-Silicon Compiler – L                                    | CTS102 | CTOS112 |

#### Chip Planning

|   |        |        |
|---|--------|--------|
| Cadence <sup>®</sup> InCyte Chip Estimator L  | CPS100 | CICE51 |
| Cadence <sup>®</sup> InCyte Chip Estimator XL | CPS200 | CICE51 |



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### Verification Bundle

#### The Verification Bundle Software Reference List

| <b><u>Product</u></b>  | <b><u>Product No.</u></b> | <b><u>Release Stream</u></b> |
|--|---------------------------|------------------------------|
| <b>Functional Verification</b>                                   |                           |                              |
| Incisive Enterprise Simulator - L                                | 29610                     | INCISIV122                   |
| Cadence <sup>®</sup> Simulation Analysis Environment (SimVision) | 25010                     | INCISIV122                   |
| Verifault – XL Simulator   | 26500                     | INCISIV122                   |
| Incisive <sup>™</sup> Enterprise Simulator                       | 29651                     | INCISIV122                   |
| Enterprise Simulator - XL Interface for MTI                      | 29661                     | INCISIV122                   |
| Enterprise Simulator - XL Interface for VCS                      | 29671                     | INCISIV122                   |
| Incisive <sup>™</sup> Formal Verifier                            | 23560                     | INCISIV122                   |
| Incisive <sup>™</sup> Enterprise Verifier – XL                   | IEV101                    | INCISIV122                   |
| Incisive <sup>™</sup> Software Extensions                        | ISX100                    | INCISIV122                   |
| Virtuoso <sup>®</sup> AMS Designer Verification Option           | 70030                     | INCISIV122                   |
| AMS Designer with Flexible Analog Simulation                     | 70020                     | INCISIV122                   |
| Cadence <sup>®</sup> System Creator                              | CSC100                    | INCISIV122                   |
| Cadence <sup>®</sup> Software Developer                          | CSD100                    | INCISIV122                   |
| <b>Verification Process Automation</b>                           |                           |                              |
| Incisive <sup>™</sup> Enterprise Manager                         | EMG100                    | INCISIV122                   |
| <b>Pre-verified, Re-usable Verification IP Components</b>        |                           |                              |
| Incisive VIP Portfolio   | VIP100                    | VIPP92                       |



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### Silicon-Package-Board Bundle

#### The Packaging & Board Bundle Software Reference List

| <b><u>Product</u></b>   | <b><u>Product No.</u></b> | <b><u>Release Stream</u></b> |
|---|---------------------------|------------------------------|
| <b>PCB Design and Layout</b>  |                           |                              |
| Allegro <sup>®</sup> PCB Designer                                     | PA3100                    | SPB166                       |
| Allegro <sup>®</sup> PCB High-Speed Option                            | PA3110                    | SPB166                       |
| Allegro <sup>®</sup> PCB Miniaturization Option                       | PA3120                    | SPB166                       |
| Allegro <sup>®</sup> Design Authoring High-Speed Option               | PA1410                    | SPB166                       |
| Allegro <sup>®</sup> PCB Routing Option                               | PS3500                    | SPB166                       |
| <br>  |                           |                              |
| Allegro <sup>®</sup> PCB Librarian - XL                               | PX3500                    | SPB166                       |
| Allegro <sup>®</sup> 2 FPGA System Planner Option                     | PA8250                    | SPB166                       |
| <br>  |                           |                              |
| <b>PCB High-Speed Analysis</b>  |                           |                              |
| Allegro <sup>®</sup> PCB SI - XL                                      | PX3100                    | SPB166                       |
| <br>  |                           |                              |
| <b>IC Packaging</b>   |                           |                              |
| Cadence <sup>®</sup> SiP Layout – XL                                  | SIP225                    | SPB166                       |
| <br>  |                           |                              |
| <b>Simulation</b>   |                           |                              |
| Allegro <sup>®</sup> AMS Simulator <sup>1</sup>                       | PS2200                    | SPB166                       |
| <br>  |                           |                              |
| <b>Sigrity Products</b>   |                           |                              |
| Allegro <sup>®</sup> Sigrity SI Base                                  | PA5700                    | ASI166                       |
| Allegro <sup>®</sup> Sigrity Package Assessment and Extraction Option | SIGR945                   | ASI166                       |

<sup>1</sup> superset of Pspice and Advanced Analysis Option



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### Changes made for 2013:

| <b>Discontinued products</b>                              | <b>Replacement</b>                       | <b>Product#</b> | <b>Release</b> |
|---|--|-----------------|----------------|
| 34570 - Virtuoso <sup>®</sup> Analog VoltageStorm Option  | Virtuoso <sup>®</sup> Power System – L   | VPS100          | IC615          |
| VST1 – VoltageStorm (translator)                          | Virtuoso <sup>®</sup> Power System – XL  | VPS200          | IC615          |
| SIP215 - Cadence <sup>®</sup> SiP Digital SI-XL           | Allegro <sup>®</sup> Sigrity SI Base     | PA5700          | ASI166         |
| SIP410 - Cadence <sup>®</sup> SiP RF Architect – XL       | Cadence <sup>®</sup> SiP Layout – XL     | SIP225          | SPB166         |
| SIP125 - Cadence <sup>®</sup> SiP Digital Architect – GXL | Cadence <sup>®</sup> SiP Layout – XL     | SIP225          | SPB166         |
| SIP625 - Cadence <sup>®</sup> Chip Integration Option     | Cadence <sup>®</sup> SiP Layout – XL     | SIP225          | SPB166         |
| ET009 - Encounter <sup>™</sup> Diagnostics Engine – XL    | Encounter <sup>™</sup> Diagnostics Basic | ET010           | ET121          |
| RC300 - Encounter <sup>™</sup> RTL Compiler - GXL option  | Encounter <sup>™</sup> RTL Compiler      | RC310           | RC121          |
| RC400 - Encounter <sup>™</sup> RTL Compiler with physical | Encounter <sup>™</sup> RTL Compiler Adv  | RC340           | RC121          |

### New products added in 2013:

|   | <b>Product #</b> | <b>Release</b> |
|---|------------------|----------------|
| Virtuoso <sup>®</sup> DFM Option                                      | 95311            | IC615          |
| Encounter <sup>™</sup> DFM GXL Option                                 | EDS50            | MVS121         |
| Litho Physical Analyzer   | LPA108           | MVS121         |
| Distributed Process for 8 CPUs  | LPA109           | MVS121         |
| Cadence <sup>®</sup> QRC Display Technology GXL Option                | QRCX330          | PVE121         |
| Allegro <sup>®</sup> Sigrity SI Base                                  | PA5700           | ASI166         |
| Allegro <sup>®</sup> Sigrity Package Assessment and Extraction Option | SIGR945          | ASI166         |
| Encounter <sup>™</sup> Diagnostics Basic                              | ET010            | ET121          |
| Encounter <sup>™</sup> RTL Compiler – L                               | RC110            | RC121          |
| Encounter <sup>™</sup> RTL Compiler Low Power Option                  | RC310            | RC121          |
| Encounter <sup>™</sup> RTL Compiler Advanced Physical Option          | RC340            | RC121          |